

PALM INTRANET

Day : Tuesday
Date: 8/16/2005
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Inventor Name Search Result

Your Search was:

Last Name = SCHEUERMANN

First Name = W.

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60421543</u>	Not Issued	159	10/24/2002	RECONFIGURABLE FILTER NODE FOR AN ADAPTIVE COMPUTING MACHINE	SCHEUERMANN, W. JAMES
<u>60420762</u>	Not Issued	160	10/22/2002	RECONFIGURABLE FILTER NODE FOR AN ADAPTIVE COMPUTING MACHINE	SCHEUERMANN, W. JAMES
<u>60391874</u>	Not Issued	159	06/25/2002	DIGITAL PROCESSING ARCHITECTURE FOR AN ADAPTIVE COMPUTING MACHINE	SCHEUERMANN, W. JAMES
<u>60388249</u>	Not Issued	159	06/12/2002	ACM INFRASTRUCTURE ALLOWS FAST TIME-TO-MARKET SOCS	SCHEUERMANN, W. JAMES
<u>11125854</u>	Not Issued	019	05/09/2005	PROCESSOR FOR VIDEO DATA ENCODING/DECODING	SCHEUERMANN, W. JAMES
<u>11125852</u>	Not Issued	019	05/09/2005	PROCESSOR FOR VIDEO DATA	SCHEUERMANN, W. JAMES
<u>10459859</u>	Not Issued	020	06/12/2003	METHOD AND SYSTEM FOR REDUCING THE TIME-TO-MARKET CONCERNS FOR EMBEDDED SYSTEM DESIGN	SCHEUERMANN, W. JAMES ✓
<u>10443596</u>	Not Issued	030	05/21/2003	PROCESSING ARCHITECTURE FOR A RECONFIGURABLE ARITHMETIC NODE	SCHEUERMANN, W. JAMES ✓
<u>10443554</u>	Not Issued	041	05/21/2003	UNIFORM INTERFACE FOR A FUNCTIONAL NODE IN AN ADAPTIVE COMPUTING ENGINE	SCHEUERMANN, W. JAMES ✓
<u>10443501</u>	Not Issued	020	05/21/2003	HARDWARE TASK MANAGER	SCHEUERMANN, W. JAMES ✓
<u>10402691</u>	Not Issued	041	03/28/2003	METHOD AND SYSTEM FOR RECONFIGURABLE CHANNEL CODING	SCHEUERMANN, W. JAMES ✓
<u>10386896</u>	Not Issued	030	03/11/2003	RECONFIGURABLE FILTER NODE FOR AN ADAPTIVE COMPUTING MACHINE	SCHEUERMANN, W. JAMES ✓

<u>10241511</u>	Not Issued	161	09/10/2002	METHOD AND SYSTEM FOR AN INTERCONNECTION NETWORK TO SUPPORT COMMUNICATIONS AMONG A PLURALITY OF HETEROGENEOUS PROCESSING ELEMENTS	SCHEUERMANN, W. JAMES	✓
<u>10092859</u>	Not Issued	030	03/06/2002	METHOD AND SYSTEM FOR DATA FLOW CONTROL OF EXECUTION NODES OF AN ADAPTIVE COMPUTING ENGINE (ACE)	SCHEUERMANN, W. JAMES	✓
<u>10022776</u>	Not Issued	161	12/13/2001	COMPUTER PROCESSOR ARCHITECTURE SELECTIVELY USING FINITE-STATE-MACHINE FOR CONTROL CODE EXECUTION	SCHEUERMANN, W. JAMES	✓
<u>09916161</u>	<u>6587057</u>	150	07/25/2001	HIGH PERFORMANCE MEMORY EFFICIENT VARIABLE-LENGTH CODING DECODER	SCHEUERMANN, W. JAMES	✓
<u>09898350</u>	Not Issued	030	07/03/2001	METHOD AND SYSTEM FOR AN INTERCONNECTION NETWORK TO SUPPORT COMMUNICATIONS AMONG A PLURALITY OF HETEROGENEOUS PROCESSING ELEMENTS	SCHEUERMANN, W. JAMES	✓
<u>09851543</u>	<u>6577678</u>	150	05/08/2001	METHOD AND SYSTEM FOR RECONFIGURABLE CHANNEL CODING	SCHEUERMANN, W. JAMES	✓

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SCHEUERMANN	W.	<input type="button" value="Search"/>

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